

## **PRODUCT SPECIFICATION**

Customer	
Project	
Part No.	Z35017-P40-总成 3
Remarks	□APPOVAL FOR SPECIFICATION ONLY  ■APPOVAL FOR SPECIFICATION AND SAMPLE

CUSTOMER			Z	HUNYIKE	П
Approved	Checked	Prepared	Approved	Checked	Prepared
Ву	Ву	Ву	Ву	Ву	Ву
				Chi Tin	Yang
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## **Revision Record**

Rev. No.	Date	Description
V1.0	2023-04-06	Preliminary Specification Release.

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## 1. General Specifications

No.	Item	Specification	Unit
1	Display Size (Diagonal)	3.5	inch
2	Display Resolution	320(H) × RGB × 480 (V)	pixels
3	Pixel Pitch	153(H) × 153 (V)	um
4	LCM Outline Dimension (Without FPC)	53.76 (W) ×82.39 (H) ×2.23 (T)	mm
5	CTP Outline Dimension (Without FPC)	55.46 (W) ×83.94 (H) ×4.25 (T)	mm
6	LCD Outline Dimension	51.96 (W) × 80.22 (H) × 0.8 (T)	mm
7	LCD Active Area	48.96 (W) ×73.44 (H)	mm
8	View Direction (Gray Inversion)	FULL VIEW	-
9	Driver IC	ILI9488	-
10	Pixel Arrangement	RGB-Stripe	-
11	Display Mode	Normal Black	-
12	Pixel driving Element	a-Si TFT	-
13	LCD Transmittance	Typ.: 4.05% Min:3.45%	-
14	LCD Contrast Ratio	Typ.: 1000 Min:800	-
15	FPC Version	Z35017-P40 V3	-
16	Interface	MCU18BIT/SPI+ 18BIT RGB	-
17	Operating Temperature	-20°C∼ 70°C	-
18	Storage Temperature	-30°C∼ 80°C	-
19	Backlight Arrangement	LED/3 Series 2 Parallel (6 lights in total)	-
20	Luminance	220	nit
21	Weight	0.035	kg

## 2. Interface Definition Description

### 2.1 LCM PIN

PIN NO.	PIN DEF.	FUNCTION DESC.					
1	GND	Ground					
2-3	VCI	Power supply for analog circuit					
4	IM0	Interface mode select PIN					
5	IM1	Interface mode select PIN					
6	IM2	Interface mode select PIN					
7	RESET	Reset pin					
8	VSYNC	Vertical synchronizing input signal for RGB interface					
9	HSYNC	Horizontal synchronizing input signal for RGB interface					
10	PCLK	Dot clock signal for RGB interface operation					
11	DE	Data enable signal for RGB interface operation					
12-29	DB17-DB0	DATA BUS					
30	GND	Ground					
31	DOUT	SPI interface output pin					
32	DIN_SDA	SPI interface input pin					
33	RDX	Read enable in 8080 MCU parallel interface					
34	WRX _SCL	Write enable in MCU parallel interface In SPI mode, this pin is used as SCL					
35	DCX	Display data/command selection (RS) pin in MCU interface					
36	CSX	Chip selection pin					
37	LEDA	LED Anode					
38	LEDK1	LED Cathode					
39	LEDK2	LED Cathode					
40	GND	Ground					

RESET voltage should be consistent with VDDI voltage, or there probably is black screen fault when power on.

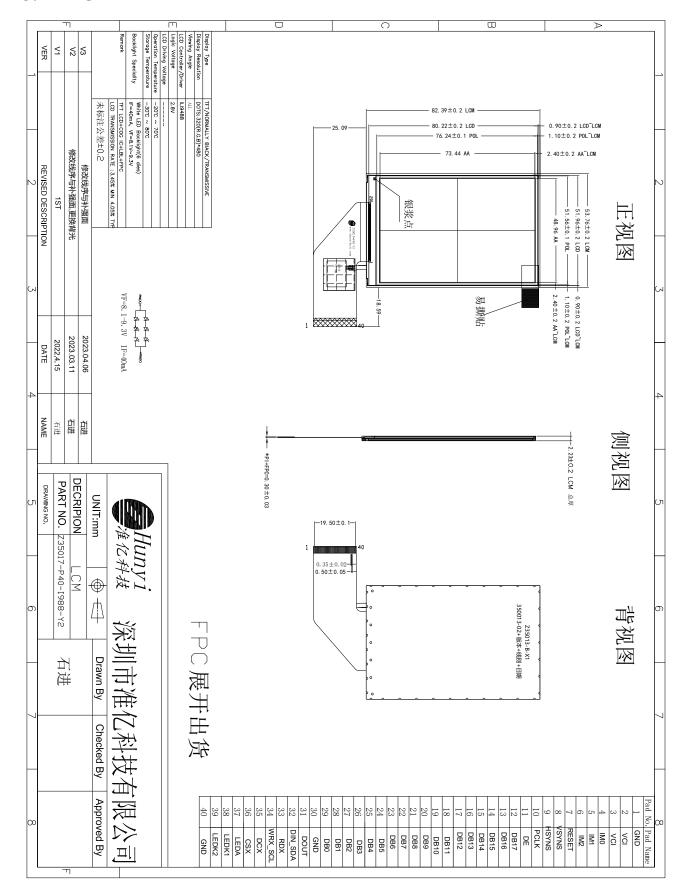
#### 2.1 TP PIN

PIN NO.	PIN DEF.	FUNCTION DESC.
1	TP-VDD(3.3V)	TP POWER SUPPLY(3.3V)
3	TP-GND	Ground
4	TP-SCL(3.3V)	TP I2C clock (SCL) clock (SCLK)(3.3V)
4	TP-SDA(3.3V)	TP I2C data (SDA) data input (MOSI)(3.3V)
5	TP-INT(3.3V)	TP Interrupt PIN(3.3V)
6	TP-RESET(3.3V)	TP Reset PIN(3.3V)



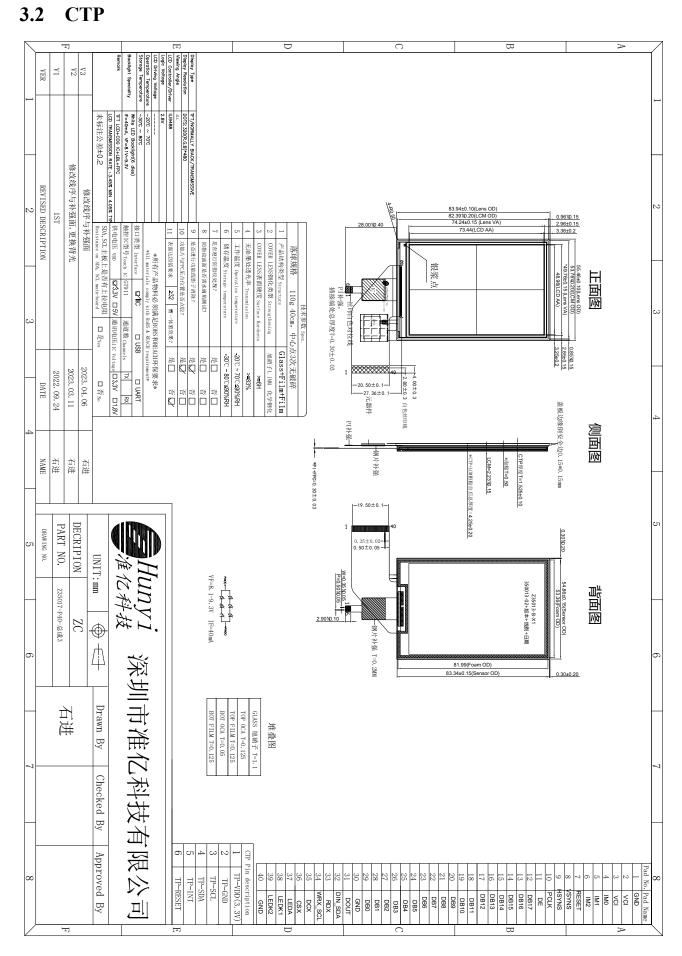
### 3. Mechanical Drawing

### 3.1 LCM





#### 3.2 **CTP**



## 4. Electrical Specifications

### 4.1. LCD Optical Charcteristics

Item		Symbol Conditions		Specification			Unit	Noto
Item	item		Conditions	Min.	Тур.	Max.	Unit	Note
Transmittance (V	With PL)	T(%)	Viouina	3.45	4.05	-	%	-
Contrast Ratio		CR	Viewing normal angle	800	1000	-	-	-
Response T	Response Time		x = y = 0	-	25	35	ms	-
				80	85	-		
Viewing Angle	Hor.	Θx- Θy+	CR ≥ 10	80	85	-	1	
Viewing Angle	Ver.		at 25℃	80	85	-	deg.	-
	ver.	Θу-		80	85	-		

### 4.2. Electrical Specifications

Item	Symbol	S	Unit		
item	Symbol	Min.	Тур.	Max.	Unit
Power For Analog Circuit	AVDD	-	-	-	V
TFT Gate On Voltage	VGH	10	12	14	V
TFT Gate Off Voltage	VGL	-14	-12	-10	V
TFT Common Electrode Voltage	Vcom	-2	-	0	V

## 4.3. Typical Operating Conditions

Item	Symbol	Min.	Тур.	Max.	Unit
Analog Supply Voltage	VCI	2.5	2.8	3.3	V
I/O Supply Voltage	1.8V	1.65	1.8	3.3	V
Input High Voltage	VIH	0.7 × VCI	-	VCI	V
Input Low Voltage	VIL	-0.3	-	0.3 × VCI	V



Output High Voltage	VOH	0.8 × VCI	-	VCI	V
Output Low Voltage	VOL	0	-	0.2 × VCI	V

## 4.4. Backlight Circuit Specifications

Item		Symbol	Min.	Тур.	Max.	Unit	Test Condition
Current		$I_{\mathrm{B}}$	-	40	-	mA	-
Voltage		$V_{\rm f}$	8.1	9	9.3	V	-
LCM Unifor	mity	-	80	-	-	%	10.40
Life Tim	e	-	30000	-	-	Hr.	If=40mA
Power Consur	nption	PBL	-	360	-	mW	
	- 1	Rx	0.611	0.626	0.641		
	Red	Ry	0.325	0.340	0.355		
7.00.6		Gx	0.294	0.309	0.324		
LCM	Green	Gy	0.589	0.604	0.619		Average the brightness
Chromaticity		Bx	0.127	0.142	0.157		EV at 9 points, Optical
Coordinate	Blue	Ву	0.035	0.050	0.065		Instrument BM-7
		Wx	0.272	0.287	0.302		
	White	Wy	0.304	0.319	0.334		

## 4.5. LCD Power Consumption

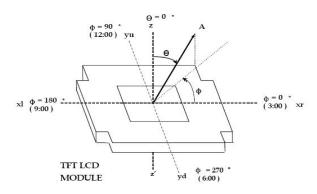
Mode	Symbol	Тур.	Max.	Unit
Normal Mode	VCI+1.8V -		-	mA
Test Condition: VCI=3.3V.				
Interface Drive Type: row flipping or column flipping.				
IPS Type LCD Panel => All Black Pattern.				
TN Type LCD Panel => All White Pattern.				
Temperature: 25°C.				
Mode	Symbol	Тур.	Max.	Unit



Sleep Mode	VCI+1.8V	-	-	μА
Test Condition: VCI=3.3V.				
DC/DC converter is enabled. Internal oscillator is started and panel scanning is started.				
Except for the IC internal crystal oscillator and panel scanning, other functions are suspended.				
Temperature: 25°C.				

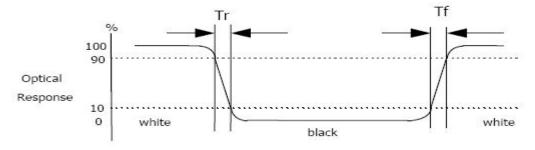
### 4.6. Measuring System

#### 4.6.1. LCM Viewing Angle



Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface.

#### 4.6.2. Response Time



Response time is the time required for the display to transition from white to black (Rising time, Tr) and from black to white (Falling time, Tf) for additional information.

#### 4.6.3. Contrast Ratio (CR)

Contrast Ratio (CR) is defined mathematically as:

Contrast Ratio =  $\frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$ 

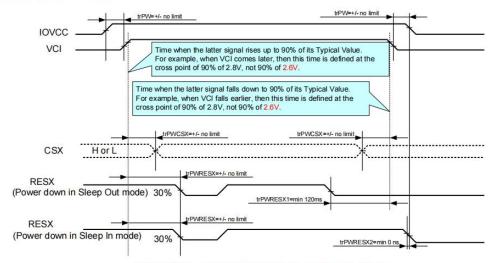
Surface luminance is the center point across the LCD surface 500mm from the surface with all pixels displaying white.

#### 4.7. Power On / Power Off

#### 4.7.1. Power On/Off

#### Case 1 – RESX Line is Held High or Unstable by Host at Power ON

If the RESX line is held High or unstable by the host during Power On, then Hardware Reset must be applied after both VCI and IOVCC have been applied. Otherwise, the correct functionality is not guaranteed. There is no timing restriction upon this hardware reset.

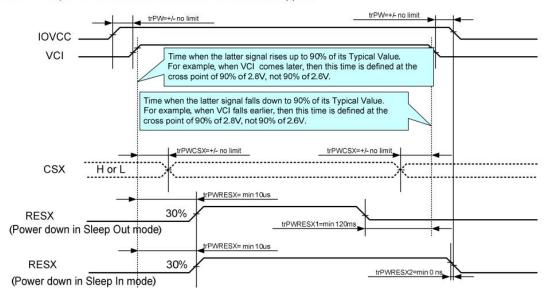


trPWRESX1 is applied to RESX falling in the Sleep Out Mode trPWRESX2 is applied to RESX falling in the Sleep In Mode

Note: Unless otherwise specified, timings herein show the cross point at 50% of the signal power level.

### Case 2 - RESX Line is Held Low by Host at Power ON

If the RESX line is held Low (and stable) by the host during Power On, then the RESX must be held low for a minimum of 10µsec after both VCI and IOVCC have been applied.



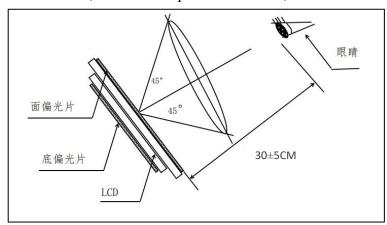
trPWRESX1 is applied to RESX falling in the Sleep Out Mode trPWRESX2 is applied to RESX falling in the Sleep In Mode

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### 5. Delivery Inspection

### 5.1. Quality Inspection Environmental Conditions

- 5.1.1. Viewing distance: the normal viewing distance between the screen and the inspector is 30±5cm; Inspection Angle: 90°±45° (90° indicates that the inspector's perspective is perpendicular to the product to be inspected).
- 5.1.2. Visual inspection illumination: 1000±200LUX;Electrical inspection illumination: 200±100LUX;Ambient temperature 25±5°C, ambient humidity 55±15%RH.



#### 5.2. Quality Inspection Standard

No.	Defect	Standard		Defect Grade	Result
			Φ ≤ 0.10mm	Ignore	OK
		< 7 inches	0.10mm < Φ ≤ 0.20mm	Minor Defect	OK
	Spot Defect		DS ≥ 10mm		
	(including bright		Φ > 0.20mm	Serious Defect	NG
1	1 spot / color spot /		$\Phi \leq 0.15$ mm	Ignore	OK
	bubble / dark		$0.15 \text{mm} < \Phi \le 0.25 \text{mm}$		
	spot, etc.)	≥ 7 inches	DS ≥ 10mm	Minor Defect	OK
			Φ > 0.25mm	Serious Defect	NG
		Φ: defect diamet	ter. DS: spacing.		
2	Linear Defect	< 7 inches	W≤0.02mm,	Langua	OK
	(scratches,	/ inches	L: unlimited	Ignore	UK



	filaments, etc.)		0.02mm < W	/ ≤ 0.03mm	Minor Defect	OK
			L ≤ 5mm			
			W > 0.0	03mm	Serious Defect	NG
			$W \le 0.0$	03mm	Ignore	OK
			L: unlimited			
		≥ 7 inches	$0.03\text{mm} < W \leq 0.05\text{mm}$		Minor Defect	OK
			L ≤ 5	mm		
			W > 0.0	05mm	Serious Defect	NG
		W: defect width	. L: defect length. D	S: spacing.		
		Display Area	Judge by S	pot Defect		
3	Polarizer Bubble	Black Edge	The distance from	_	Minor defect	OK
		Area The distance from the edge of the display area is less than 0.5mm.		Judge by Spot Defect		
4	Polarizer Bump (Mark)	Display Area / Black Edge Area	Invisible when the touch screen or cover plate is assembled.		Minor Defect	OK
		Item	Method	Instrument	Median	Tolerance Range
				0 1 1	According to the	
			x, y Color	Optical	actual test on the	1004
5 Color and Luminance	Color	Coordinate	Instrument BM-7	sample confirmed	± 0.04	
				by the customer.		
		Average the	Optical	According to the		
		Luminance	brightness EV at		actual test on the	± 20%
			9 points		sample confirmed	
			•		by the customer.	
6	6 Other Standards Subject to the negotiation by both parties.					



7	Warranty Period	One year after sale.	
8	Guarantee	ROHS、REACH	
9	Websites	Official: <a href="https://en.zhunyikeji.com/">https://en.zhunyikeji.com/</a> Globle Resources: <a href="https://zhunyi.manufacturer.globalsources.com/">https://zhunyikeji.en.alibaba.com/</a> Alibaba: <a href="https://zhunyikeji.en.alibaba.com/">https://zhunyikeji.en.alibaba.com/</a> 1688: <a href="https://shop9641057ru80o3.1688.com/">https://shop9641057ru80o3.1688.com/</a>	

### 6. Reliability Test

Item	Condition	Result Determination
High-Temperature Storage	80°С 120Н	
Low-Temperature Storage	-30°C 120H	After the test, leave the LCD
High-Temperature Operation	70°C 120H	samples indoors at normal
Low-Temperature Operation	-20°C 120H	temperature and humidity for 2H for function and
High-Temperature and High-Humidity	60°C 90%RH 120H	appearance inspection.  The sample should meet the
Thermal Cycling Test	-30°C/0.5H ~ +80°C/0.5H 100 cycles in total	requirements on electrical performance, but be free from
Vibration Test	Frequency: 10Hz ~ 55Hz ~ 10Hz  Amplitude: 0.75mm  Cycle once a minute,30cycles in total  (Packing Condition)	the following defects:  1. Air bubble in the module,  2. No display,  3. Glass crack.
ESD Test	$\pm 4$ kV Human Body Mode 150pF/330Ω $\pm 8$ kV Air Mode 150pF/330Ω	

#### Note:

- 1) Each module under test can only be used for one of the test items.
- 2) The quantity of samples for each test item is 2.
- 3) Fault Judgment Criterion: Basic Specifications, Electrical Specifications, Mechanical Specifications, Optical Specifications.



#### 7. Precautions

- 7.1. The display screen consists of glass and polarizer. Since the glass is fragile, the user must pay special attention to the edge area, and protect it from falling, vibration, or mechanical impact.
- 7.2. If the display screen is damaged and the liquid crystal material leaks, be sure not to get any in the mouth. If the liquid crystal material contacts the skin or clothes, flush off with soap and water.
- 7.3. Do not apply excessive force to the display screen or the joint part, or the color will change. Do not touch the display screen with bare hands, which will stain the display area and degraded insulation between terminals (some of the appearance is determined by the polarizer).
- 7.4. The polarizer covering the display panel of the LCD module is soft and easy to be scratched, be sure to handle carefully. Do not touch, impact, press, or rub the exposed polarizers with anything harder than an HB pencil lead (e.g.: glass, tweezers, etc.). Do not place or attach anything onto the display area to avoid leaving marks. The condensed material on the surface or terminals due to cold will damage or stain the polarizer. After the test in low temperature environment, the product must be warmed up in a container before put into the room temperature environment.
- 7.5. If the display panel is stained, blow warm air onto the surface and gently wipe it with a soft and dry cloth. If it is seriously contaminated, wipe it with a wet cloth dipped in one of the following solvents:
  - glycerol
  - ethyl Alcohol

Do not scrub, and avoid damaging the display panel.

- 7.6. Solvents other than those mentioned above may damage the polarizer. In particular, never use any of the following solvents:
  - water
  - ketone
  - arene

Wipe off saliva or water drop immediately, the contact with water over a long period of time may cause deformation or color fading. Avoid contact with oil or grease.

- 7.7. Special note: minimize electrode corrosion. Because electrode corrosion can be accelerated by water droplets, condensation of humidity, or electrification in a high humidity environment.
- 7.8. Assemble the LCD Module by the mounting holes. Make sure the LCD module make sure there is no bending, distortion, or deformation. Do not forcibly pull or bend the transmission wire or the backlight wire.

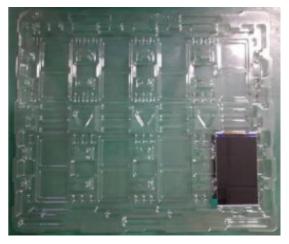


- 7.9. Do not disassemble the LCD module.
- 7.10. NC terminal should be disconnected. Do not connect any device.
- 7.11. If the logic circuit power supply is off, do not send the input signal.
- 7.12. Since the LCD module is integrated with CMOS, pay special attention to the modules. To prevent electrostatic damage, be careful to maintain an suitable work environment.
  - Make sure the module has the same potential as the human body before take the LCD module out
    of the packing box for assembly. The reliable grounding is necessary during module processing.
  - The required tool, such as the electric soldering iron, must be reliably grounded. Make sure the it is connected to AC power supply, and no electric leakage. When fixing the module with electric screwdriver, it must be grounded, to reduce the electromagnetic wave generated by the electric commutator spark as much as possible.
  - Do not assemble or operate under dry condition to reduce the static electricity. To reduce static electricity, the workplace must not be too dry. The recommended relative humidity is 50 60%.
     Keep your work clothes and work table grounded as much as possible
  - The LCD module is coated with a film to protect the display surface. Be careful when peeling off
     the film to reduce the generated static electricity.
- 7.13. Since the LCD module has high precision assembly and regulation, try to avoid excessive impact on the module or making any changes:
  - Do not change the shape of the tab on the metal frame.
  - Do not drill any extra hole, modify the shape, or change the position of component on the printed circuit board.
  - Do not change or damage the pattern on the printed circuit board.
  - Never modify the zebra strip (conductive rubber) or heat seal connector.
  - Do not make any change with the electric soldering iron except for the joint.
  - Do not throw, bend or twist.

### 8. Packing and Storage

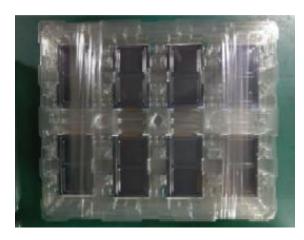
#### 8.1. Packing Method

Step 1



Take 1pcs of the product, put it into a anti-static bag.

Step 2



Take 2 bags of product to place into the carton, make sure they are surface to surface. Put a piece of EPE pad between the carton and the separator to protect the products.

Step 3



Put the products into cartons one by one, each carton contains 60 pieces of products.

Step 4



The cartons should be taped and shipped with labels.

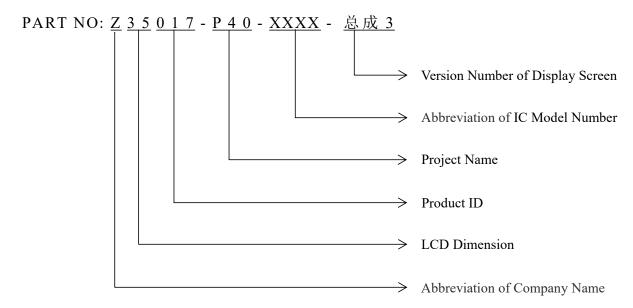
### 8.2. Storage Method

Store in an ambient temperature of 23±5°C, and in a relative humidity of 60±15%. The storage period should not exceed 12 months. Do not expose to the sun for a long period of time.

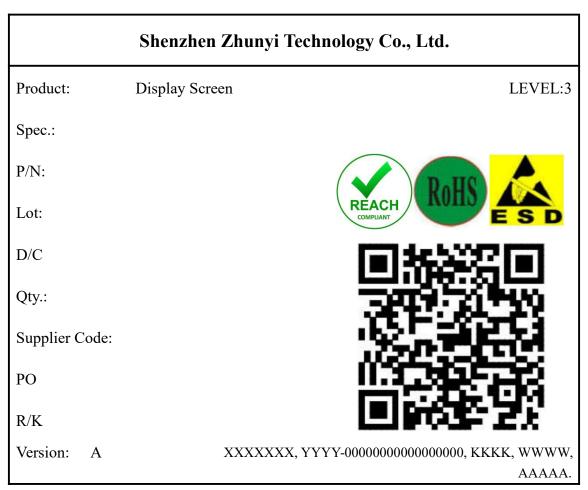
- 8.2.1. Store in clean environment, free from dust, active gas, or solvent.
- 8.2.2. Store in anti-static environment.



#### 8.3. Nomenclature



#### 8.4. Label





## 8.5. Product appearance identification

Item	Description	Production QR Code  Position Display
QR Code Content	www.zhunyikeji.com 13823705290	Z35013-B-X1 350013-02+版本+线别+日期 6M403V6646 FIREITIEFE LA (WO-084-10000000000000000000000000000000000
Printing Code appearance and content	Z35017-P40-总成 3 YYMMDD+Time+5 digits serial number	
1. Customer have detail position and direction		
requirements(Refer to right picture for details).		
2. Control conten		