PRODUCT SPECIFICATION

Customer	
Project	
Part No.	**Z30010-P40F-R-771S-Y6**
Remarks	□APPOVAL FOR SPECIFICATION ONLY ■APPOVAL FOR SPECIFICATION AND SAMPLE

CUSTOMER			**ZHUNYIKEJI**		
Approved By	Checked By	Prepared By	Approved By	Checked By	Prepared By
					YAN, Yan

Revision Record

Rev. No.	Date	Description
V1.0	2018-1-12	Preliminary Specification Release.
V2.0	2018-5-26	Drawings Updated.
V3.0	2019-11-15	Drawings Updated.

Contents

1. General Specifications

No.	Item	Standard Value	Unit
1	Resolution	480 × RGB × 854	pixel
2	LCM Outline Dimension	42.7 (W) ×74.7 (H) × 2.5 (T)	mm
3	LCD Outline Dimension	40.33 (W) × 72.86 (H) ×1.0 (T)	mm
4	LCD Active Area	37.44 × 66.61	mm
5	Pixel Pitch	0.026 × 0.078	mm
6	Display Mode	Normal Black	
7	Pixel Arrangement	RGB Vertical Stripe	
8	Viewing Angle	FREE	
9	Color Configuration	RGB	
10	LCD Transmittance	Typ.: 3.5% Min.: 3.1%	
11	LCD Contrast Ratio	Typ.: 1000;Min.:800	
12	FPC Version	Z30010-P40F-R V6	
13	Interface	SPI+RGB	
14	Operating Temperature	-20°C~ 70°C	
15	Storage Temperature	-30°C~ 80°C	
16	Backlight Arrangement	LED/6 series(6 lights in total)	
17	Luminance	-	nit
18	Weight	-	

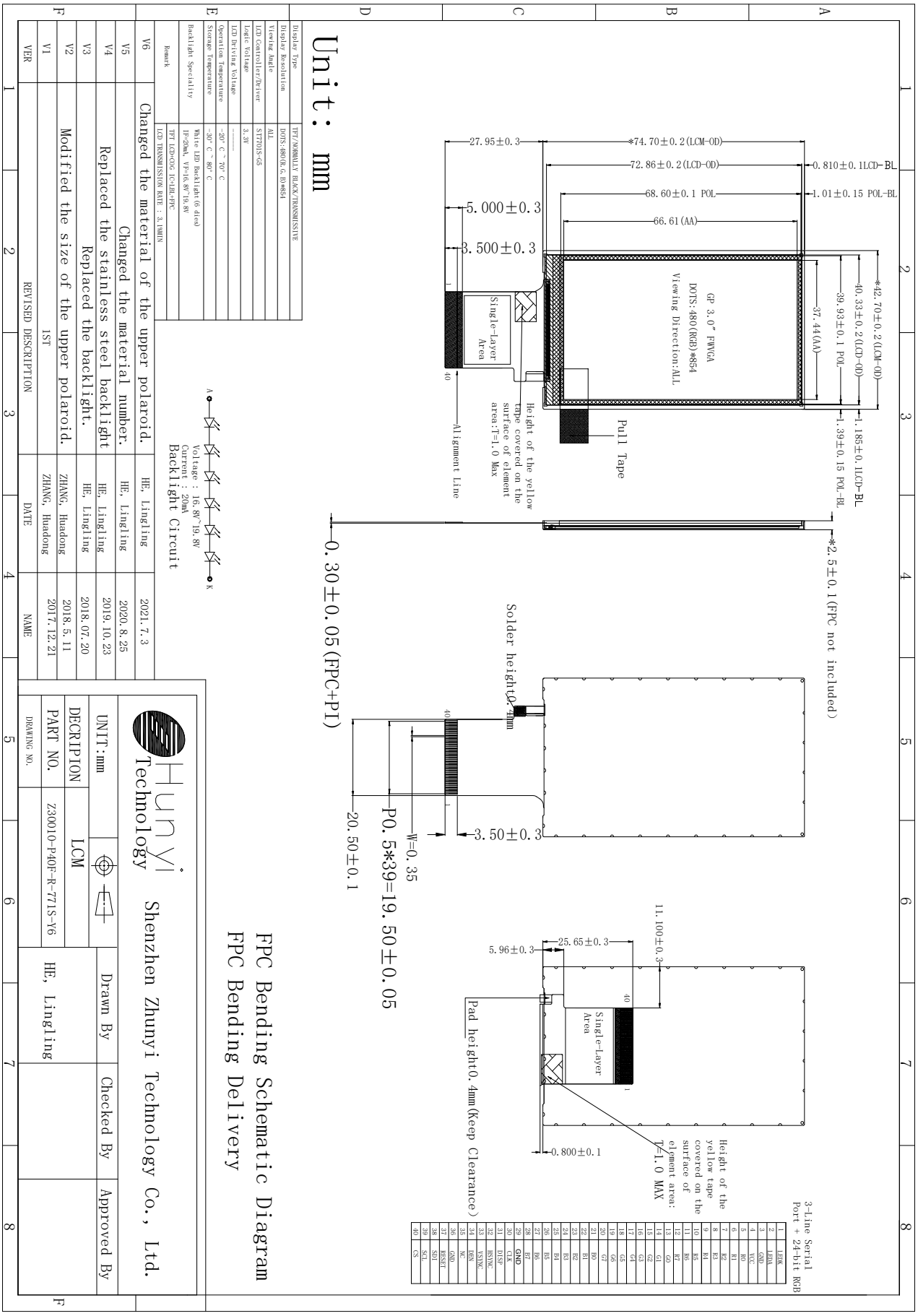
Zhunyi Technology can promise the consistency of the same batch of products. Therefore, there is no commitment to consistency between different batches.

2. Interface Definition Description

PIN NO.	PIN DEF.	FUNCTION DESC.
1	LED-K	POWER SUPPLY- FOR BACKLIGHT CATHODE
2	LED-A	POWER SUPPLY+ FOR BACKLIGHT ANODE
3	GND	Ground
4	VCI	POWER SUPPLY（2.8V）
5-12	R0-R7	Data bus
13-20	G0-G7	Data bus
21-28	B0-B7	Data bus
29	GND	Ground
30	CLK	Pixel clock signal in RGB I/F
31	NC	NC
32	HSYNC	Horizontal sync. Signal in RGB I/F.
33	VSYNC	Vertical sync. Signal in RGB I/F.
34	DEN	Data enable signal in RGB I/F DE mode.
35	NC	NC
36	GND	Ground
37	RESET	Reset Signal pin（2.8V）
38	SDI	Serial input signal in SPI I/F
39	SCL	A synchronous clock signal in SPI I/F
40	CS	Chip Select input pin. (Active Low)

RESET voltage should be consistent with VDDI voltage, or there probably is black screen fault when power on.

3. Drawing


Unit: mm
Display Type: TFT/NORMALLY BLACK/TRANSMISSIVE
Display Resolution: DOTS:480(R.G.B)*854
Viewing Angle: ALL
LCD Controller/Driver: ST7701S-G5
Logic Voltage: 3.3V
LCD Driving Voltage: ------
Operation Temperature: -20° C ~ 70° C
Storage Temperature: -30° C ~ 80° C
Backlight Specialty: White LED Backlight (6 dies) IF=20mA, VF=16.8V~19.8V
Remark: TFT LCD+COG IC+FPC LCD TRANSMISSION RATE : 3.19MUN
V6 Changed the material of the upper polaroid. HE, Lingling 2021.7.3
V5 Changed the material number. HE, Lingling 2020.8.25
V4 Replaced the stainless steel backlight HE, Lingling 2019.10.23
V3 Replaced the backlight. HE, Lingling 2018.07.20
V2 Modified the size of the upper polaroid. ZHANG, Huadong 2018.5.11
V1 1ST ZHANG, Huadong 2017.12.21
VER REVISED DESCRIPTION DATE NAME
*42.70±0.2(LCM-OD)
40.33±0.2(LCD-OD)
39.93±0.1 POL
37.44(AA)
27.95±0.3
*74.70±0.2(LCM-OD)
72.86±0.2(LCD-OD)
68.60±0.1 POL
66.61(AA)
5.000±0.3
3.500±0.3
0.810±0.1LCD-BL
1.01±0.15 POL-BL
1.185±0.1LCD-BL
1.39±0.15 POL-BL
GP 3.0" FWVGA
DOTS:480(RGB)*854
Viewing Direction:ALL
Single-Layer Area
Pull Tape
Height of the yellow tape covered on the surface of element area:T=1.0 Max
Alignment Line
*2.5±0.1(FPC not included)
0.30±0.05(FPC+PI)
Solder height0.4mm
3.50±0.3
P0.5*39=19.50±0.05
W=0.35
20.50±0.1
A
K
Voltage : 16.8V~19.8V
Current : 20mA
Backlight Circuit
11.100±0.3
25.65±0.3
5.96±0.3
Pad height0.4mm(Keep Clearance)
0.800±0.1
3-Line Serial Port + 24-bit RGB
1 LEDK
2 LEDA
3 GND
4 VCC
5 R0
6 R1
7 R2
8 R3
9 R4
10 R5
11 R6
12 R7
13 G0
14 G1
15 G2
16 G3
17 G4
18 G5
19 G6
20 G7
21 B0
22 B1
23 B2
24 B3
25 B4
26 B5
27 B6
28 B7
29 GND
30 CLK
31 DISP
32 HSYNC
33 VSYNC
34 DEN
35 NC
36 SDA
37 RESET
38 SCL
39 NC
40 CS
FPC Bending Schematic Diagram
FPC Bending Delivery
Hunyi Technology
Shenzhen Zhunyi Technology Co., Ltd.
UNIT:mm
DECRIPTION LCM
PART NO. Z30010-P40F-R-771S-V6
DRAWING NO.
Drawn By HE, Lingling
Checked By
Approved By

4. Electrical Specifications

4.1. DC Specifications

Item	Symbol	Specification			Unit
		Min.	Typ.	Max.	
TFT Gate On Voltage	VGH	14	15	16	V
TFT Gate Off Voltage	VGL	-12	-11	-10	V
TFT Common Electrode Voltage	Vcom	-2	-	0	V

4.2. Typical Operating Conditions

Item	Symbol	Min.	Typ.	Max.	Unit
Analog Supply Voltage	VCI	2.7	2.8	3.3	V
Digital Supply Voltage	VDD	2.7	2.8	3.3	V
I/O Supply Voltage	IOVCC	1.65	1.8	3.3	V
Input High Voltage	VIH	0.8 × IOVCC	-	IOVCC	V
Input Low Voltage	VIL	0	-	0.2 × IOVCC	V
Output High Voltage	VOH	0.8 × IOVCC	-	-	V
Output Low Voltage	VOL	-	-	0.2 × IOVCC	V

4.3. Backlight Circuit Specifications

Item	Symbol	Min.	Typ.	Max.	Unit
Current	I_B	-	20	-	mA
Voltage	V_f	16.8	18	19.8	V

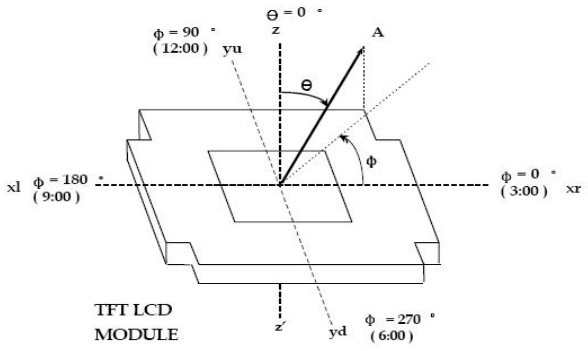
Power Consumption	PBL	-	360	-	mW

4.4. LCD Power Consumption

Mode	Symbol	Typ.	Max.	Unit
Normal Mode	VCI+IOVCC	-	-	mA
Test Condition: VCI=2.8V，IOVCC=1.8V.				
Interface Drive Type: row flipping or column flipping.				
IPS Type LCD Panel => All Black.				
TN Type => All White.				
Temperature: 25°C.				
Mode	**Symbol**	**Typ.**	**Max.**	**Unit**
Sleep Mode	VCI+IOVCC	-	-	μA
Test Condition: VCI=2.8V, IOVCC=1.8V.				
DC/DC converter is enabled. Internal oscillator is started and panel scanning is started.				
Except for the IC internal crystal oscillator and panel scanning, other functions are suspended.				
Temperature: 25°C.				

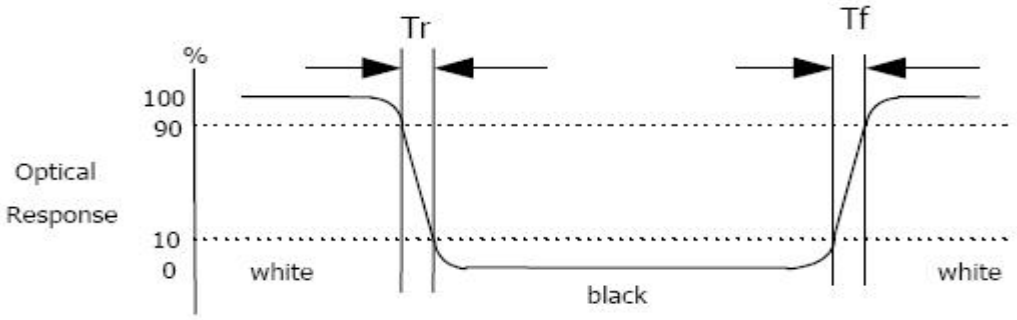
4.5. Measuring System

4.5.1. LCM Viewing Angle



Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface.

4.5.2. Response Time



Response time is the time required for the display to transition from white to black (Rising time, Tr) and from black to white (Falling time, Tf) for additional information.

4.5.3. Contrast Ratio (CR)

Contrast Ratio (CR) is defined mathematically as:

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$$

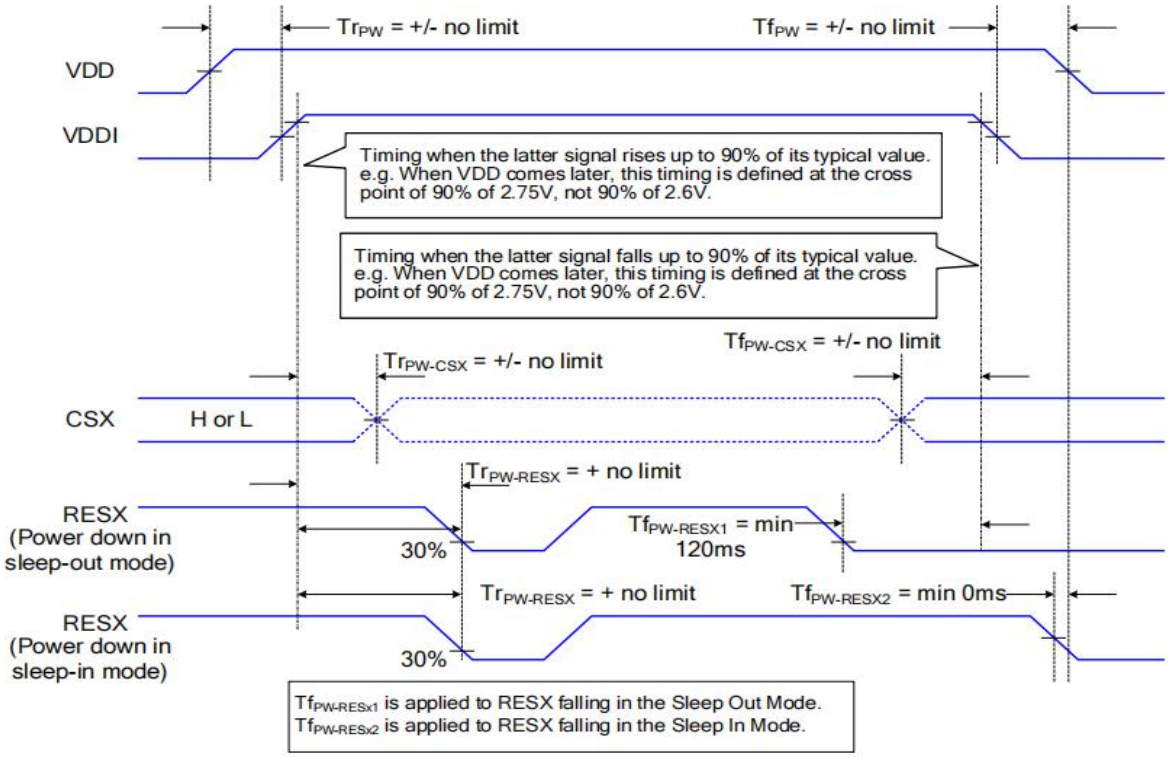
Surface luminance is the center point across the LCD surface 500mm from the surface with all pixels displaying white.

4.6. Power On / Power Off

VDDI and VDDA can be applied or powered down in any order. During the Power Off sequence, if the LCD is in the Sleep Out mode, VDDA and VDDI must be powered down with minimum 120msec. If the LCD is in the Sleep In mode, VDDA and VDDI can be powered down with minimum 0msec after the RESX is released. CSX can be applied at any timing or can be permanently grounded. RESX has high priority over CSX.

Notes:

1. There will be no damage to the ST7701S if the power sequences are not met.
2. There will be no abnormal visible effects on the display panel during the Power On/Off Sequences.
3. There will be no abnormal visible effects on the display between the end of Power On Sequence and before receiving the Sleep Out command, and also between receiving the Sleep In command and the Power Off Sequence.
4. If the RESX line is not steadily held by the host during the Power On Sequence as defined in Sections 9.1 and 9.2, then it will be necessary to apply the Hardware Reset (RESX) after the completion of the Host Power On Sequence to ensure correct operations. Otherwise, all the functions are not guaranteed. The power on/off sequence is illustrated below



5. Delivery Inspection

No.	Defect	Standard		Defect Grade	Result
1	Spot Defect (including bright spot / color spot / bubble / dark spot, etc.)	< 7 inches (excluding 6.95 and 7 inches)	Φ ≤ 0.15mm	Ignore	OK
			0.15mm < Φ ≤ 0.20mm DS ≥ 10mm Qty.: unlimited	Minor Defect	OK
			Φ > 0.20mm	Serious Defect	NG
		> 7 inches (including 6.95 and 7 inches)	Φ ≤ 0.20mm	Ignore	OK
			0.20mm < Φ ≤ 0.30mm DS ≥ 10mm Qty.: unlimited	Minor Defect	OK
			Φ > 0.30mm	Serious Defect	NG
		Φ: defect diameter. DS: spacing.			

<table>
<tr><td rowspan="6">2</td><td rowspan="6">Linear Defect (scratches, filaments, etc.)</td><td rowspan="3">< 7 inches (excluding 6.95 and 7 inches)</td><td colspan="2">W≤0.03mm,
L: unlimited</td><td>Ignore</td><td>OK</td></tr>
<tr><td colspan="2">0.03mm < W ≤ 0.05mm
L ≤ 3mm
DS ≥ 10mm
Qty.: unlimited</td><td>Minor Defect</td><td>OK</td></tr>
<tr><td colspan="2">W > 0.05mm</td><td>Serious Defect</td><td>NG</td></tr>
<tr><td rowspan="3">> 7 inches (including 6.95 and 7 inches)</td><td colspan="2">W ≤ 0.03mm
L: unlimited</td><td>Ignore</td><td>OK</td></tr>
<tr><td colspan="2">0.03mm < W ≤ 0.05mm
L ≤ 5mm
DS ≥ 10mm
Qty.: unlimited</td><td>Minor Defect</td><td>OK</td></tr>
<tr><td colspan="2">W > 0.05mm</td><td>Serious Defect</td><td>NG</td></tr>
<tr><td colspan="5">W: defect width. L: defect length. DS: spacing.</td></tr>
<tr><td rowspan="3">3</td><td rowspan="3">Polarizer Bubble</td><td>Display Area</td><td colspan="2">Judge by Spot Defect</td><td></td><td></td></tr>
<tr><td rowspan="2">Black Edge Area</td><td colspan="2">The distance from the edge of the display area is greater than 0.5mm.</td><td>Minor defect</td><td>OK</td></tr>
<tr><td colspan="2">The distance from the edge of the display area is less than 0.5mm.</td><td colspan="2">Judge by Spot Defect</td></tr>
<tr><td>4</td><td>Polarizer Bump (Mark)</td><td>Display Area / Black Edge Area</td><td colspan="2">Invisible when the touch screen or cover plate is assembled.</td><td>Minor Defect</td><td>OK</td></tr>
<tr><td rowspan="2">5</td><td rowspan="2">Color and Luminance</td><td>Item</td><td>Method</td><td>Instrument</td><td>Median</td><td>Tolerance Range</td></tr>
<tr><td>Color</td><td>x, y Color Coordinate</td><td>Optical Instrument BM-7</td><td>According to the actual test on the sample confirmed by the customer.</td><td>± 0.04</td></tr>
</table>

		Luminance	Average the brightness EV at 9 points	Optical Instrument BM-7	According to the actual test on the sample confirmed by the customer.	± 20%
6	Other Standards	Subject to the negotiation by both parties.				
7	Warranty Period	One year after sale.				
8	Websites	Official: https://en.zhunyikeji.com/ Globle Resources: https://zhunyi.manufacturer.globalsources.com/ Alibaba: https://zhunyikeji.en.alibaba.com/ 1688: https://shop9641057ru80o3.1688.com/				

6. Reliability Test

Item	Condition	Result Determination
High-Temperature Storage	80°C 240h	After the test, leave the LCD samples indoors at normal temperature and humidity for 2 - 4h for function and appearance inspection. The sample should meet the requirements on electrical performance, but be free from the following defects: 1. Air bubble in the module, 2. No display, 3. Glass crack.
Low-Temperature Storage	-30°C 240h	
High-Temperature Operation	70°C 240h	
Low-Temperature Operation	-20°C 240h	
High-Temperature and High-Humidity	60°C 90%RH 240h	
Thermal Cycling Test	-30°C/0.5h ~ +80°C/0.5h 24 cycles in total	
Vibration Test	Frequency: 10Hz ~ 55Hz ~ 10Hz Amplitude: 1.5mm x, y, z direction for 1h in total (Packing Condition)	
ESD Test	±4kV Human Body Mode 150pF/330Ω ±8kV Air Mode 150pF/330Ω	

Note:

1) **Each module under test can only be used for one of the test items.**
2) **The quantity of samples for each test item is 2.**
3) **Fault Judgment Criterion: Basic Specifications, Electrical Specifications, Mechanical Specifications, Optical Specifications.**

7. Precautions

7.1. The display screen consists of glass and polarizer. Since the glass is fragile, the user must pay special attention to the edge area, and protect it from falling, vibration, or mechanical impact.

7.2. If the display screen is damaged and the liquid crystal material leaks, be sure not to get any in the mouth. If the liquid crystal material contacts the skin or clothes, flush off with soap and water.

7.3. Do not apply excessive force to the display screen or the joint part, or the color will change. Do not touch the display screen with bare hands, which will stain the display area and degraded insulation between terminals (some of the appearance is determined by the polarizer).

7.4. The polarizer covering the display panel of the LCD module is soft and easy to be scratched, be sure to handle carefully. Do not touch, impact, press, or rub the exposed polarizers with anything harder than an HB pencil lead (e.g.: glass, tweezers, etc.). Do not place or attach anything onto the display area to avoid leaving marks. The condensed material on the surface or terminals due to cold will damage or stain the polarizer. After the test in low temperature environment, the product must be warmed up in a container before put into the room temperature environment.

7.5. If the display panel is stained, blow warm air onto the surface and gently wipe it with a soft and dry cloth. If it is seriously contaminated, wipe it with a wet cloth dipped in one of the following solvents:

- glycerol
- ethyl Alcohol

Do not scrub, and avoid damaging the display panel.

7.6. Solvents other than those mentioned above may damage the polarizer. In particular, never use any of the following solvents:

- water
- ketone
- arene

Wipe off saliva or water drop immediately, the contact with water over a long period of time may cause deformation or color fading. Avoid contact with oil or grease.

7.7. Special note: minimize electrode corrosion. Because electrode corrosion can be accelerated by water droplets, condensation of humidity, or electrification in a high humidity environment.

7.8. Assemble the LCD Module by the mounting holes. Make sure the LCD module make sure there is no bending, distortion, or deformation. Do not forcibly pull or bend the transmission wire or the backlight wire.

7.9. Do not disassemble the LCD module.

7.10. NC terminal should be disconnected. Do not connect any device.

7.11. If the logic circuit power supply is off, do not send the input signal.

7.12. Since the LCD module is integrated with CMOS, pay special attention to the modules. To prevent electrostatic damage, be careful to maintain an suitable work environment.

- Make sure the module has the same potential as the human body before take the LCD module out of the packing box for assembly. The reliable grounding is necessary during module processing.
- The required tool, such as the electric soldering iron, must be reliably grounded. Make sure the it is connected to AC power supply, and no electric leakage. When fixing the module with electric screwdriver, it must be grounded, to reduce the electromagnetic wave generated by the electric commutator spark as much as possible.
- Do not assemble or operate under dry condition to reduce the static electricity. To reduce static electricity, the workplace must not be too dry. The recommended relative humidity is 50 - 60%. Keep your work clothes and work table grounded as much as possible
- The LCD module is coated with a film to protect the display surface. Be careful when peeling off the film to reduce the generated static electricity.

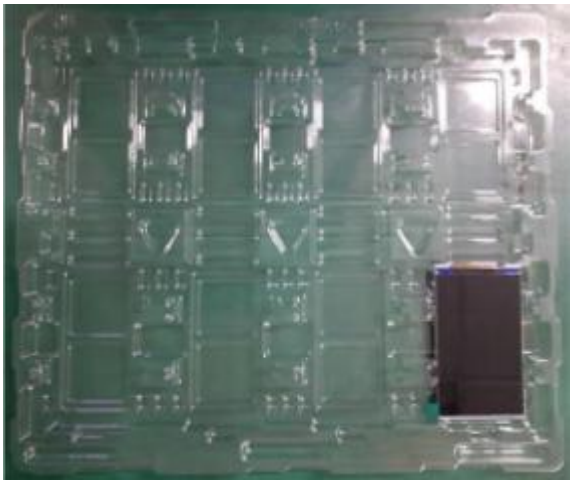
7.13. Since the LCD module has high precision assembly and regulation, try to avoid excessive impact on the module or making any changes:

- Do not change the shape of the tab on the metal frame.
- Do not drill any extra hole, modify the shape, or change the position of component on the printed circuit board.
- Do not change or damage the pattern on the printed circuit board.
- Never modify the zebra strip (conductive rubber) or heat seal connector.
- Do not make any change with the electric soldering iron except for the joint.
- Do not throw, bend or twist.

8. Packing and Storage

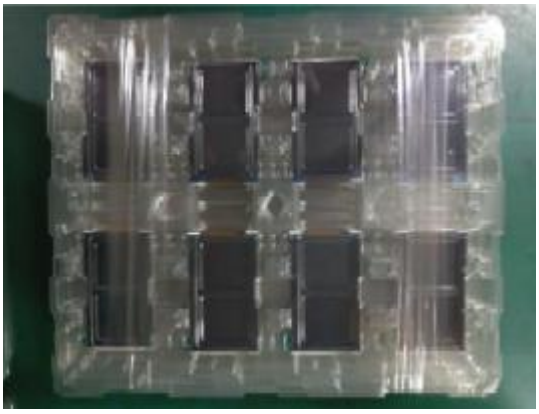
8.1. Packing Method

Step 1



Put the products into a vacuum formed tray one by one. Each vacuum formed tray can hold 8 products in total.

Step 2



Layer the vacuum formed trays in stagger, and then wrap them with the tape.

Step 3

Put the packaged products into the carton, and fix the vacuum formed trays with the EPE to protect the products.

Step 4

The cartons should be taped and shipped with labels.

8.2. Storage Method

Store in an ambient temperature of 23±5°C, and in a relative humidity of 60±15%. The storage period should not exceed 12 months. Do not expose to the sun for a long period of time.

8.2.1. Store in clean environment, free from dust, active gas, or solvent.

8.2.2. Store in anti-static environment.

8.3. Nomenclature

PART NO: Z 4 3 0 1 4 - P 4 0 - S 7 8 3 - Y 1

- Version Number of Display Screen
- Abbreviation of IC Model Number
- Project Name
- Product ID
- LCD Dimension
- Abbreviation of Company Name

8.4. Label

Shenzhen Zhunyi Technology Co., Ltd.		
Product:	Display Screen	LEVEL:3
Spec.:		
P/N:		
Lot:		
D/C		
Qty.:		
Supplier Code:		
PO		
R/K		
Version: A		XXXXXXX, YYYY-00000000000000000, KKKK, WWWW, AAAAA.