

PRODUCT SPECIFICATION

Customer	
Project	
Part No.	Z69005-P40-Y1
Remarks	<input type="checkbox"/> APPOVAL FOR SPECIFICATION ONLY <input checked="" type="checkbox"/> APPOVAL FOR SPECIFICATION AND SAMPLE

CUSTOMER			ZHUNYIKEJI		
Approved	Checked	Prepared	Approved	Checked	Prepared
By	By	By	By	By	By
				Shi Jin	Yang Zifeng

Revision Record

Rev. No.	Date	Description
V1.0	2024-3-14	Preliminary Specification Release.

Contents

1. General Specifications	3
2. Interface Definition Description	4
3. Mechanical Drawing	5
4. Electrical Specifications	6
5. Delivery Inspection	13
6. Reliability Test	15
7. Precautions	16
8. Packing and Storage	18

1. General Specifications

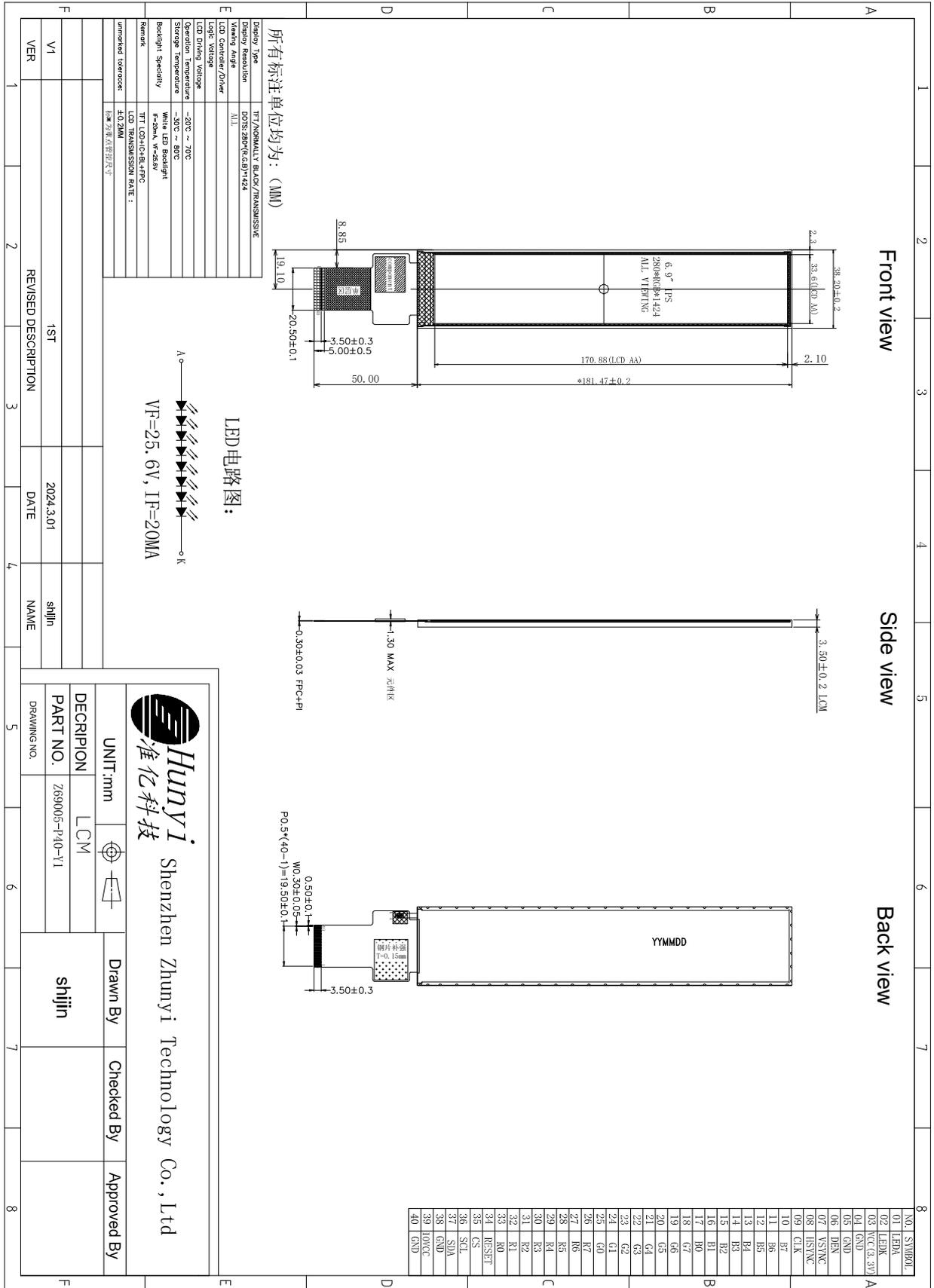
No.	Item	Specification	Unit
1	Display Size (Diagonal)	7.0	inch
2	Display Resolution	280(H) × RGB × 1424 (V)	pixels
3	Pixel Pitch	143.2(H) × 143.2 (V)	um
4	LCM+CTP Outline Dimension (Without FPC)	N/A	mm
5	LCM Outline Dimension (Without FPC)	38.2 (W) × 181.47 (H) × 3.5(T)	mm
6	LCD Active Area	33.6 (W) × 170.88 (H)	mm
7	View Direction (Gray Inversion)	FULL VIEW	-
8	Driver IC	NV3052C	-
9	Pixel Arrangement	RGB-Stripe	-
10	Display Mode	Normal Black	-
11	Pixel driving Element	a-Si TFT	-
12	LCD Transmittance	Typ.:4.1%	-
13	LCD Contrast Ratio	Typ.: 9 0 0 Min:1200	-
14	FPC Version	Z69005 V1	-
15	Interface	3 SPI+24bit RGB	-
16	Operating Temperature	-20°C~ 70°C	-
17	Storage Temperature	-30°C~ 80°C	-
18	Backlight Arrangement	LED	-
19	Luminance	Typ.:350	nit
20	Weight	TBD	kg

2. Interface Definition Description

PIN NO.	PIN DEF.	FUNCTION DESC.
1	LEDA	POWER SUPPLY- FOR BACKLIGHT ANODE(+)
2	LEDK	POWER SUPPLY- FOR BACKLIGHT CATHODE(-)
3	VCC(VCI)	POWER SUPPLY (2.8V/3.3V)
4-5	GND	Ground
6	ENABLE	Data enable signal for RGB interface operation
7	VSYNC	Vertical (Frame) synchronizing input signal for RGB interface operation.
8	HSYNC	Horizontal (Line) synchronizing input signal for RGB interface
9	DCLK(PCLK)	Dot clock signal for RGB interface operation
10-17	DB7-DB0	BLUE data
18-25	G7-G0	GREEN data
26-33	R7-R0	RED data
34	RESET	Reset signal input terminal. Active at 'L' .
35	CS	LCM Chip select signal input pin
36	SCL	LCM SPI clock (SCL) clock (SCLK)
37	SDA	LCM SPI data (SDA) data input /output (MOSI)
38	GND	Ground
39	IOVCC	I/O POWER SUPPLY
40	GND	Ground

RESET voltage should be consistent with IOVCC voltage, or there probably is black screen fault when power on.

3. Mechanical Drawing



4. Electrical Specifications

4.1. LCD Optical Characteristics

Item	Symbol	Conditions	Specification			Unit	Note
			Min.	Typ.	Max.		
Transmittance (With PL)	T(%)	Viewing normal angle $x = y = 0$	4.8	5.0	-	%	-
Contrast Ratio	CR		800	1000	-	-	-
Response Time	TR+TF		-	35	40	ms	-
Viewing Angle	Hor.	Θ_{x+}	-	85	-	deg.	-
		Θ_{x-}	-	85	-		
	Ver.	Θ_{y+}	-	85	-		
		Θ_{y-}	-	85	-		

4.2. Electrical Specifications

Item	Symbol	Specification			Unit
		Min.	Typ.	Max.	
Power For Analog Circuit	AVDD	4.8	-	5.5	V
TFT Gate On Voltage	VGH	10.6	15	15.4	V
TFT Gate Off Voltage	VGL	-14	-12	-7.5	V
TFT Common Electrode Voltage	Vcom	-2	-	0	V

4.3. Typical Operating Conditions

Item	Symbol	Min.	Typ.	Max.	Unit
Analog Supply Voltage	VCI	2.7	3.3	3.6	V
I/O Supply Voltage	IOVCC	1.65	1.8	3.3	V
Input High Voltage	VIH	0.7*IOVCC	—	IOVCC	V
Input Low Voltage	VIL	GND	—	0.3*IOVCC	V

Output High Voltage	VOH	0.8*IOVCC	—	IOVCC	V
Output Low Voltage	VOL	GND	—	0.2*IOVCC	V

4.4. Backlight Circuit Specifications

Item	Symbol	Min.	Typ.	Max.	Unit	Test Condition
Current	I _B	-	20	-	mA	-
Voltage	V _f	24.0	25.0	26.4	V	-
LCM Uniformity	-	80	-	-	%	If=20mA
Life Time	-	30000	-	-	Hr.	
Power Consumption	PBL	-	500	-	mW	
LCM Chromaticity Coordinate	Red	R _x	0.63	0.65	0.67	Average the brightness EV at 9 points, Optical Instrument BM-7
		R _y	0.30	0.32	0.34	
	Green	G _x	0.26	0.28	0.30	
		G _y	0.56	0.58	0.60	
	Blue	B _x	0.12	0.14	0.16	
		B _y	0.07	0.09	0.11	
	White	W _x	0.25	0.28	0.31	
		W _y	0.26	0.29	0.32	

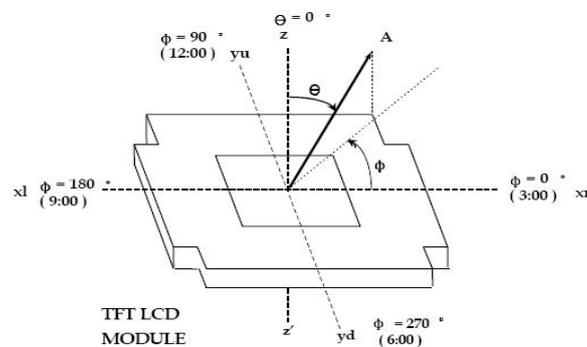
4.5. LCD Power Consumption

Mode	Symbol	Typ.	Max.	Unit
Normal Mode	VCI+IOVCC	-	-	mA
Test Condition: VCI=3.3V.				
Interface Drive Type: row flipping or column flipping.				
IPS Type LCD Panel => All Black Pattern.				
TN Type LCD Panel => All White Pattern.				
Temperature: 25°C.				
Mode	Symbol	Typ.	Max.	Unit

Sleep Mode	VCI+IOVCC	-	-	μA
Test Condition: VCI=3.3V.				
DC/DC converter is enabled. Internal oscillator is started and panel scanning is started.				
Except for the IC internal crystal oscillator and panel scanning, other functions are suspended.				
Temperature: 25°C.				

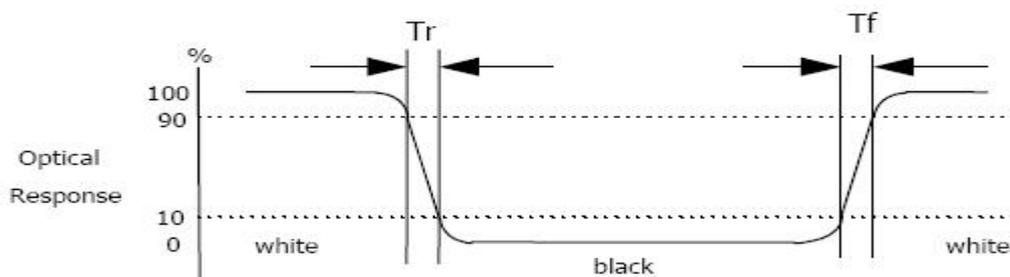
4.6. Measuring System

4.6.1. LCM Viewing Angle



Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface.

4.6.2. Response Time



Response time is the time required for the display to transition from white to black (Rising time, T_r) and from black to white (Falling time, T_f) for additional information.

4.6.3. Contrast Ratio (CR)

Contrast Ratio (CR) is defined mathematically as:

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$$

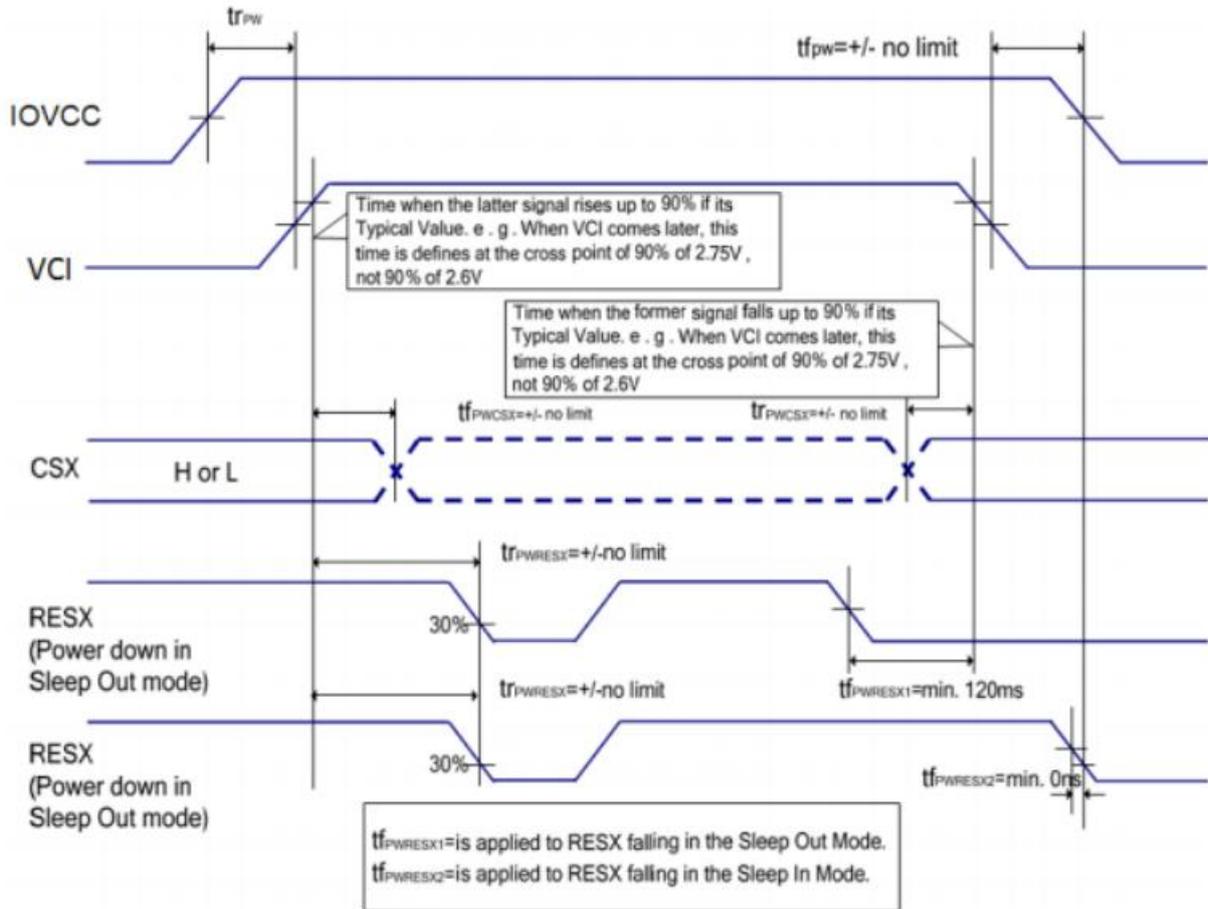
Surface luminance is the center point across the LCD surface 500mm from the surface with all pixels displaying white.

4.7. Power On / Power Off

4.7.1. Power On/Off

Case 1 – RESX line is held high or unstable by host at power on

If RESX line is held High or unstable by the host during Power On, then a Hardware Reset must be applied after both VCI and IOVCC have been applied – otherwise correct functionality is not guaranteed. There is no timing restriction upon this hardware reset.

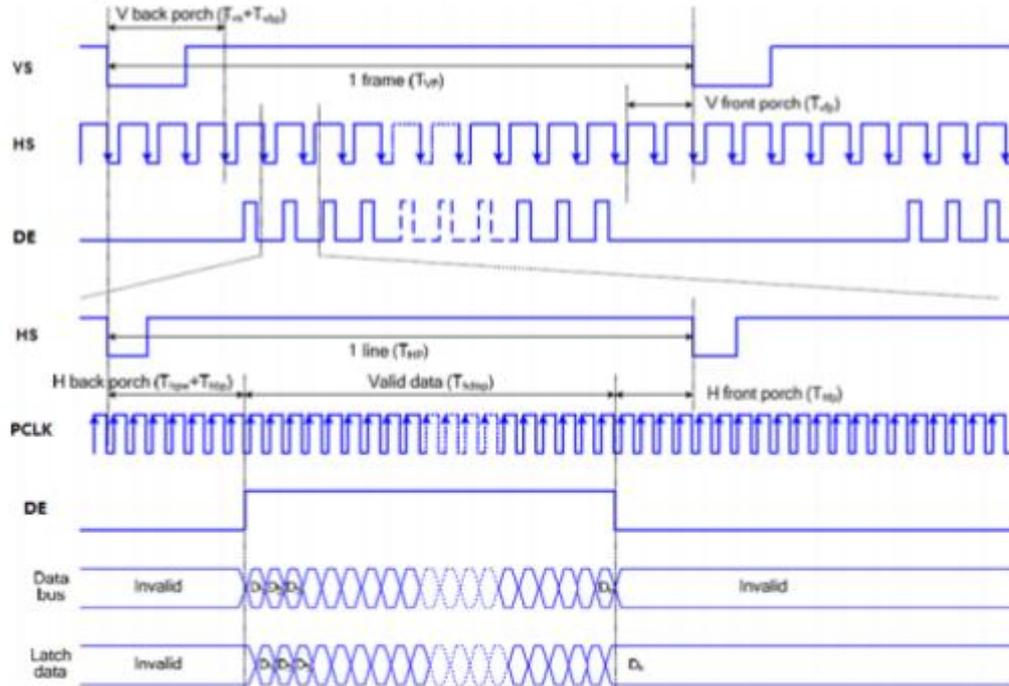


NV3052CGRB supports two kinds of RGB interface, DE mode and SYNC mode. The table shown below uses command 23h to select RGB interface mode.

sync_mode[1:0]	RGB Mode
00	SYNC+DE mode
01	SYNC mode
10	DE mode
11	SYNC+DE mode

6.1.3 RGB Interface Timing

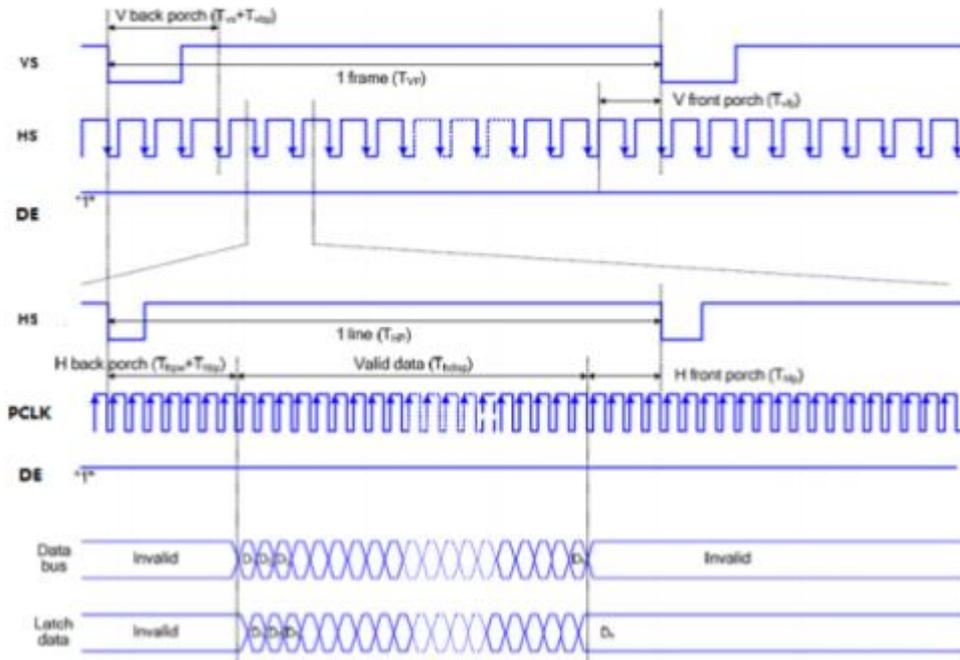
The timing chart of RGB interface DE mode is shown as follows.



Timing Chart of Signals in RGB Interface DE Mode

Note: The setting of front porch and back porch in host must match that in IC as this mode.

The timing chart of RGB interface SYNC mode is shown as follows.



Timing chart of RGB interface SYNC mode

Below Table provide the timing parameter by external Vertical-cycle

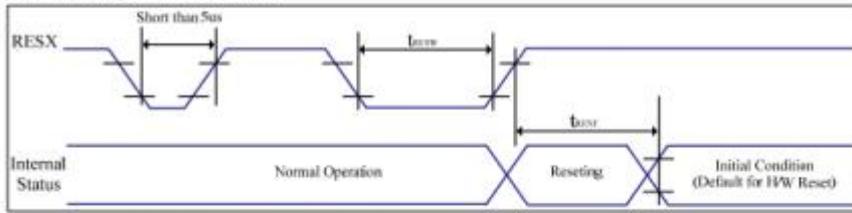
(Resolution for 720/640 horizontal x 1280 vertical display with Frame-Rate of 60Hz)

Parameters	Symbols	Min.	Typ	Max.	Unit
Horizontal Synchronization	hpw	-	2	-	PCLK
Horizontal Back Porch	hbp	-	42	-	PCLK
Horizontal Front Porch	hfp	-	44	-	PCLK
Hsync+ HBP+ HFP	-	-	88*Note1	-	PCLK
Horizontal Address (Display area)	hdisp	-	720	-	PCLK
Horizontal cycle	-	-	12.703	-	us
Vertical Synchronization	VS	-	2	-	Line
Vertical Back Porch	vbp	-	14	-	Line
Vertical Front Porch	vfp	-	16	-	Line
Vsync+ VBP+ VFP	-	-	32	-	Line
Vertical Address (Display area)	vdisp	-	1280	-	Line
Vertical cycle	-	-	16.66	16.181	ms
Frame-Rate	-	-	60	61.8	Hz

"-" means no limit.

Note : 1. If using Image Process Algorithm, Type value for H-blanking is minimum requirement.

Reset timing characteristics



VSS=0V, IOVCC=1.65V to 3.6V, VCI=2.5V to 6.0V, Ta = -30°C to 70°C

Symbol	Parameter	Related Pins	MIN	TYP	MAX	Note	Unit
T _{RES}	*1) Reset low pulse width	RESX	10	-	-	-	us
T _{RES}	*2) Reset complete time	-	-	-	5	When reset applied during Sleep in mode	ms
		-	-	-	120	When reset applied during Sleep out mode	ms

Table: Reset input timing

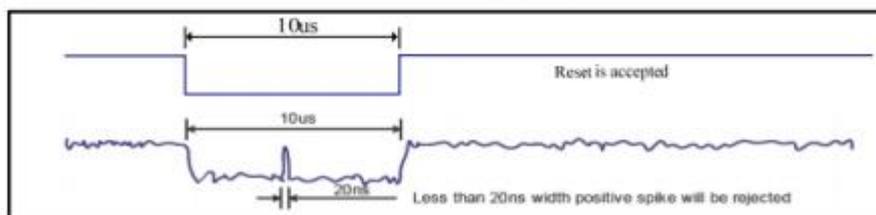
Note 1: Due to an electrostatic discharge on RESX line, spike does not cause irregular system reset according to the table below.

RESX Pulse	Action
Shorter than 5us	Reset Rejected
Longer than 10us	Reset
Between 5us and 10us	Reset starts (It depends on voltage and temperature condition.)

Note 2: During the resetting period, the display will be blanked (The display is entering blanking sequence, which maximum time is 120ms, when Reset Starts in Sleep Out mode. The display remains the blank state in Sleep In mode), then return to default condition for H/W reset.

Note 3: During Reset Complete Time, ID1/ID2/ID3 and VCOM value in OTP will be latched to internal register. After a rising edge of RESX, there is a H/W reset complete time (T_{RES}) which lasted 5ms. The loading operation will be done every time during this reset.

Note 4: Spike Rejection also applies during a valid reset pulse as shown below:



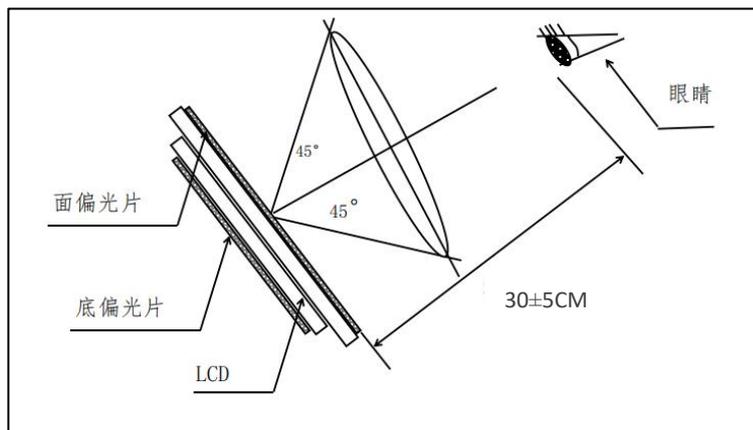
Note 5: It is necessary to wait 5msec after releasing RESX before sending commands. Also Sleep Out command cannot be sent for 120 msec.

5. Delivery Inspection

5.1. Quality Inspection Environmental Conditions

5.1.1. Viewing distance: the normal viewing distance between the screen and the inspector is $30\pm 5\text{cm}$; Inspection Angle: $90^\circ\pm 45^\circ$ (90° indicates that the inspector's perspective is perpendicular to the product to be inspected).

5.1.2. Visual inspection illumination: $1000\pm 200\text{LUX}$; Electrical inspection illumination: $200\pm 100\text{LUX}$; Ambient temperature $25\pm 5^\circ\text{C}$, ambient humidity $55\pm 15\%\text{RH}$.



5.2. Quality Inspection Standard

No.	Defect	Standard		Defect Grade	Result
1	Spot Defect (including bright spot / color spot / bubble / dark spot, etc.)	< 7 inches	$\Phi \leq 0.10\text{mm}$	Ignore	OK
			$0.10\text{mm} < \Phi \leq 0.20\text{mm}$	Minor Defect	OK
			$\text{DS} \geq 10\text{mm}$	Serious Defect	NG
		$\cong 7$ inches	$\Phi \leq 0.15\text{mm}$	Ignore	OK
			$0.15\text{mm} < \Phi \leq 0.25\text{mm}$	Minor Defect	OK
			$\text{DS} \geq 10\text{mm}$	Serious Defect	NG
Φ : defect diameter. DS: spacing.					
2	Linear Defect (scratches,	< 7 inches	$W \leq 0.02\text{mm}$, L: unlimited	Ignore	OK

	filaments, etc.)		0.02mm < W ≤ 0.03mm		Minor Defect	OK
			L ≤ 5mm			
		≧ 7 inches	W > 0.03mm		Serious Defect	NG
			W ≤ 0.03mm		Ignore	OK
			L: unlimited			
		0.03mm < W ≤ 0.05mm		Minor Defect	OK	
L ≤ 5mm						
W > 0.05mm		Serious Defect	NG			
W: defect width. L: defect length. DS: spacing.						
3	Polarizer Bubble	Display Area	Judge by Spot Defect			
		Black Edge Area	The distance from the edge of the display area is greater than 0.5mm.		Minor defect	OK
			The distance from the edge of the display area is less than 0.5mm.		Judge by Spot Defect	
4	Polarizer Bump (Mark)	Display Area / Black Edge Area	Invisible when the touch screen or cover plate is assembled.		Minor Defect	OK
5	Color and Luminance	Item	Method	Instrument	Median	Tolerance Range
		Color	x, y Color Coordinate	Optical Instrument BM-7	According to the actual test on the sample confirmed by the customer.	± 0.04
		Luminance	Average the brightness EV at 9 points	Optical Instrument BM-7	According to the actual test on the sample confirmed by the customer.	± 20%
6	Other Standards	Subject to the negotiation by both parties.				

7	Warranty Period	One year after sale.
8	Guarantee	ROHS、REACH
9	Websites	Official: https://zhunyiikeji.com/ Official: https://zhunyiidisplay.com/ Alibaba: https://zhunyiikeji.en.alibaba.com/ 1688: https://shop9641057ru80o3.1688.com/

6. Reliability Test

Item	Condition	Result Determination
High-Temperature Storage	80°C 120H	After the test, leave the LCD samples indoors at normal temperature and humidity for 2H for function and appearance inspection. The sample should meet the requirements on electrical performance, but be free from the following defects: 1. Air bubble in the module, 2. No display, 3. Glass crack.
Low-Temperature Storage	-30°C 120H	
High-Temperature Operation	70°C 120H	
Low-Temperature Operation	-20°C 120H	
High-Temperature and High-Humidity	60°C 90%RH 120H	
Thermal Cycling Test	-20°C/0.5H ~ +70°C/0.5H 100 cycles in total	
Vibration Test	Frequency: 10Hz ~ 55Hz ~ 10Hz Amplitude: 0.75mm Cycle once a minute, 30 cycles in total (Packing Condition)	
ESD Test	±4kV Human Body Mode 150pF/330Ω ±8kV Air Mode 150pF/330Ω	

Note:

- 1) Each module under test can only be used for one of the test items.
- 2) The quantity of samples for each test item is 2.
- 3) **Fault Judgment Criterion: Basic Specifications, Electrical Specifications, Mechanical Specifications, Optical Specifications.**

7. Precautions

- 7.1. The display screen consists of glass and polarizer. Since the glass is fragile, the user must pay special attention to the edge area, and protect it from falling, vibration, or mechanical impact.
- 7.2. If the display screen is damaged and the liquid crystal material leaks, be sure not to get any in the mouth. If the liquid crystal material contacts the skin or clothes, flush off with soap and water.
- 7.3. Do not apply excessive force to the display screen or the joint part, or the color will change. Do not touch the display screen with bare hands, which will stain the display area and degraded insulation between terminals (some of the appearance is determined by the polarizer).
- 7.4. The polarizer covering the display panel of the LCD module is soft and easy to be scratched, be sure to handle carefully. Do not touch, impact, press, or rub the exposed polarizers with anything harder than an HB pencil lead (e.g.: glass, tweezers, etc.). Do not place or attach anything onto the display area to avoid leaving marks. The condensed material on the surface or terminals due to cold will damage or stain the polarizer. After the test in low temperature environment, the product must be warmed up in a container before put into the room temperature environment.
- 7.5. If the display panel is stained, blow warm air onto the surface and gently wipe it with a soft and dry cloth. If it is seriously contaminated, wipe it with a wet cloth dipped in one of the following solvents:
 - glycerol
 - ethyl AlcoholDo not scrub, and avoid damaging the display panel.
- 7.6. Solvents other than those mentioned above may damage the polarizer. In particular, never use any of the following solvents:
 - water
 - ketone
 - areneWipe off saliva or water drop immediately, the contact with water over a long period of time may cause deformation or color fading. Avoid contact with oil or grease.
- 7.7. Special note: minimize electrode corrosion. Because electrode corrosion can be accelerated by water droplets, condensation of humidity, or electrification in a high humidity environment.
- 7.8. Assemble the LCD Module by the mounting holes. Make sure the LCD module make sure there is no bending, distortion, or deformation. Do not forcibly pull or bend the transmission wire or the backlight wire.

- 7.9. Do not disassemble the LCD module.
- 7.10. NC terminal should be disconnected. Do not connect any device.
- 7.11. If the logic circuit power supply is off, do not send the input signal.
- 7.12. Since the LCD module is integrated with CMOS, pay special attention to the modules. To prevent electrostatic damage, be careful to maintain a suitable work environment.
- Make sure the module has the same potential as the human body before take the LCD module out of the packing box for assembly. The reliable grounding is necessary during module processing.
 - The required tool, such as the electric soldering iron, must be reliably grounded. Make sure it is connected to AC power supply, and no electric leakage. When fixing the module with electric screwdriver, it must be grounded, to reduce the electromagnetic wave generated by the electric commutator spark as much as possible.
 - Do not assemble or operate under dry condition to reduce the static electricity. To reduce static electricity, the workplace must not be too dry. The recommended relative humidity is 50 - 60%.
Keep your work clothes and work table grounded as much as possible
 - The LCD module is coated with a film to protect the display surface. Be careful when peeling off the film to reduce the generated static electricity.
- 7.13. Since the LCD module has high precision assembly and regulation, try to avoid excessive impact on the module or making any changes:
- Do not change the shape of the tab on the metal frame.
 - Do not drill any extra hole, modify the shape, or change the position of component on the printed circuit board.
 - Do not change or damage the pattern on the printed circuit board.
 - Never modify the zebra strip (conductive rubber) or heat seal connector.
 - Do not make any change with the electric soldering iron except for the joint.
 - Do not throw, bend or twist.

8. Packing and Storage

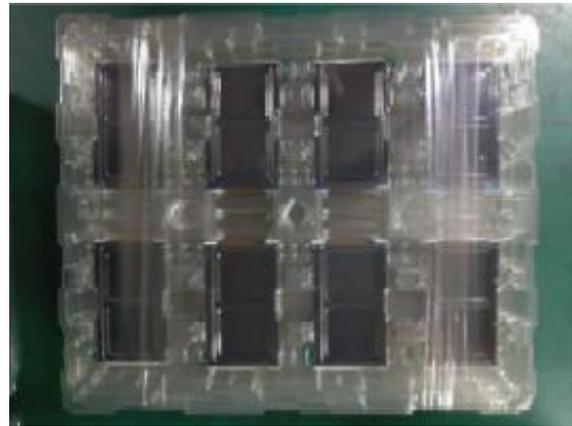
8.1. Packing Method

Step 1



Take 1pcs of the product, put it into a anti-static bag.

Step 2



Take 2 bags of product to place into the carton, make sure they are surface to surface. Put a piece of EPE pad between the carton and the separator to protect the products.

Step 3



Put the products into cartons one by one, each carton contains 60 pieces of products.

Step 4



The cartons should be taped and shipped with labels.

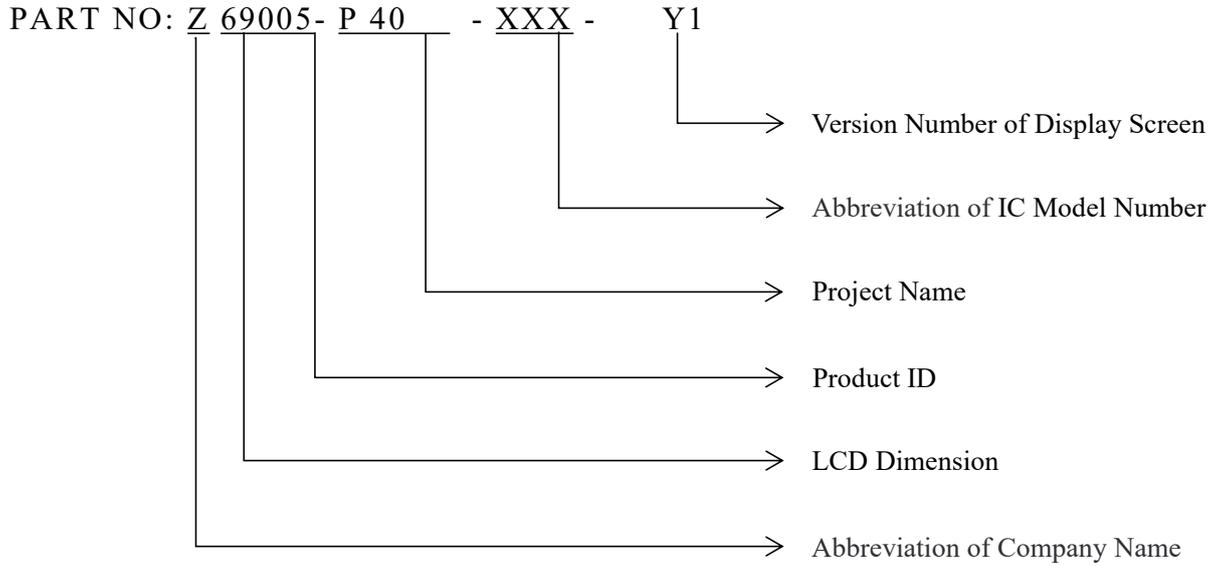
8.2. Storage Method

Store in an ambient temperature of $23\pm 5^{\circ}\text{C}$, and in a relative humidity of $60\pm 15\%$. The storage period should not exceed 12 months. Do not expose to the sun for a long period of time.

8.2.1. Store in clean environment, free from dust, active gas, or solvent.

8.2.2. Store in anti-static environment.

8.3. Nomenclature



8.4. Label

Shenzhen Zhunyi Technology Co., Ltd.		
Product:	Display Screen	LEVEL:3
Spec.:		
P/N:		
Lot:		
D/C		
Qty.:		
Supplier Code:		
PO		
R/K		
Version: A	XXXXXXXX, YYYY-0000000000000000, KKKK, WWWW,	AAAAA.





